

	Type	Hits	Search Text	DBs	Time Stamp
1	BRS	596	refresh\$3 same ((low adj power) or (sleep adj mode))	USPAT; EPO; JPO; IBM TDB	2000/12/28 06:35
2	BRS	186	EDO adj (RAM or DRAM)	USPAT; EPO; JPO; IBM TDB	2000/12/28 06:36
3	BRS	7	(refresh\$3 same ((low adj power) or (sleep adj mode))) and (EDO adj (RAM or DRAM))	USPAT; EPO; JPO; IBM TDB	2000/12/28 06:36
4	BRS	28	refresh adj timing adj generator	USPAT; EPO; JPO; IBM TDB	2000/12/28 06:46
5	BRS	0	(refresh adj generator) same (EDO adj (RAM or DRAM))	USPAT; EPO; JPO; IBM TDB	2000/12/28 06:48
6	BRS	0	(refresh adj3 generator) same (EDO adj (RAM or DRAM))	USPAT; EPO; JPO; IBM TDB	2000/12/28 06:48
7	BRS	16	(refresh adj\$3 generator) same (EDO adj (RAM or DRAM))	USPAT; EPO; JPO; IBM TDB	2000/12/28 06:48

	Document ID	Issue Date	Pages	Title	Current OR
1	US 5748547 A	19980505	24	High performance semiconductor memory devices having multiple dimension bit lines.	365/222
2	US 5825704 A	19981020	39	High performance embedded semiconductor memory devices with multiple dimension first-level bit lines	365/222
3	US 5859801 A	19990112	15	Flexible fuse placement in redundant semiconductor memory	365/200
4	US 5881016 A	19990309	14	Method and apparatus for optimizing power consumption and memory bandwidth in a video controller using SGRAM and SDRAM power reduction modes	365/230.03
5	US 6070215 A	20000530	16	Computer system with improved transition to low power operation	710/129
6	US 6108229 A	20000822	44	High performance embedded semiconductor memory device with multiple dimension first-level bit-lines	365/52
7	US 6134167 A	20001017	25	Reducing power consumption in computer memory	365/222

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